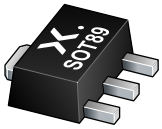


BST60-Q

PNP Darlington transistor

27 October 2023 Product data sheet

1. General description

PNP Darlington transistor in a SOT89 (SC-62) flat lead Surface-Mounted Device (SMD) plastic package.

NPN complement: BST50-Q

2. Features and benefits

- Integrated diode and resistor
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Industrial switching applications such as:
  - Print hammer
  - Solenoid
  - Relay and lamp driving

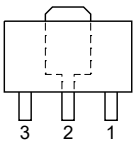
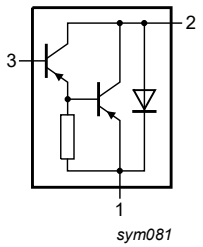
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-	-	-45	V
I_C	collector current			-	-	-1	A
h_{FE}	DC current gain	V_{CE} = -10 V; I_C = -150 mA; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		1000	-	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter	 SOT89	 sym081
2	C	collector		
3	B	base		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BST60-Q	SOT89	plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	SOT89

7. Marking

Table 4. Marking codes

Type number	Marking code
BST60-Q	BS1

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-60	V
V_{CEO}	collector-emitter voltage	open base		-	-45	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
I_C	collector current			-	-1	A
I_{CM}	peak collector current			-	-2	A
I_B	base current			-	-100	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	1.3	W
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-65	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm^2.

9. Thermal characteristics

Table 6. Thermal characteristics

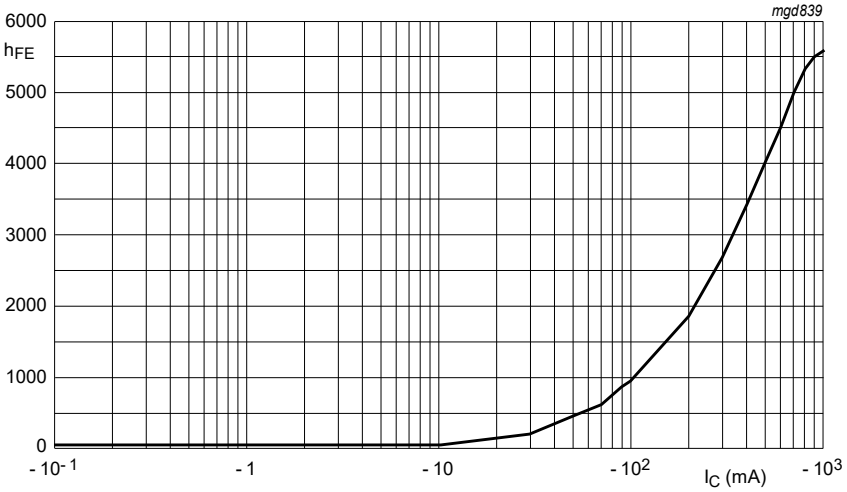
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	96	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	16	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm^2.

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I_{EBO}	emitter-base cut-off current	V_{EB} = -4 V; I_C = 0 A; T_{amb} = 25 °C		-	-	-50	nA
I_{CES}	collector-emitter cut-off current	V_{CE} = -45 V; V_{BE} = 0 V; T_{amb} = 25 °C		-	-	-50	nA
h_{FE}	DC current gain	V_{CE} = -10 V; I_C = -150 mA; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		1000	-	-	
		V_{CE} = -10 V; I_C = -500 mA; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		2000	-	-	
V_{CEsat}	collector-emitter saturation voltage	I_C = -500 mA; I_B = -0.5 mA; T_{amb} = 25 °C		-	-	-1.3	V
		I_C = 500 mA; I_B = -0.5 mA; T_j = 150 °C		-	-	-1.3	V
V_{BEsat}	base-emitter saturation voltage	I_C = -500 mA; I_B = -0.5 mA; T_{amb} = 25 °C		-	-	-1.9	V
f_T	transition frequency	V_{CE} = -5 V; I_C = -500 mA; f = 100 MHz; T_{amb} = 25 °C		-	200	-	MHz
Switching times (between 10% and 90% levels)							
t_{on}	turn-on time	I_{Bon} = -0.5 mA; I_{Boff} = 0.5 mA; I_{Con} = -500 mA; T_{amb} = 25 °C		-	500	-	ns
t_{off}	turn-off time			-	700	-	ns



V_{CE} = -10 V

Fig. 1. DC current gain; typical values

11. Test information

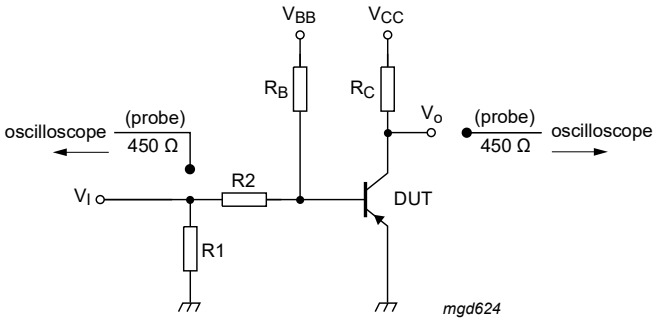


Fig. 2. Test circuit for switching times

V_i = -10 V; T = 200 µs; tp = 6 µs; $t_r = t_f \leq 3$ ns

R1 = 56 Ω; R2 = 10 kΩ; R_B = 10 kΩ; R_C = 18 Ω

V_{BB} = 1.8 V; V_{CC} = -10.7 V

Oscilloscope: input impedance Z_i = 50 Ω

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

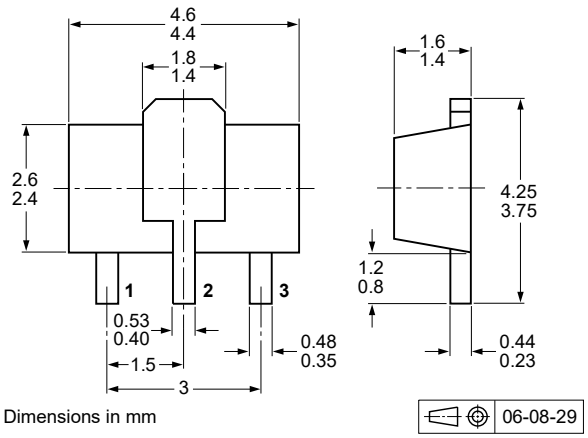


Fig. 3. Package outline SOT89

13. Soldering

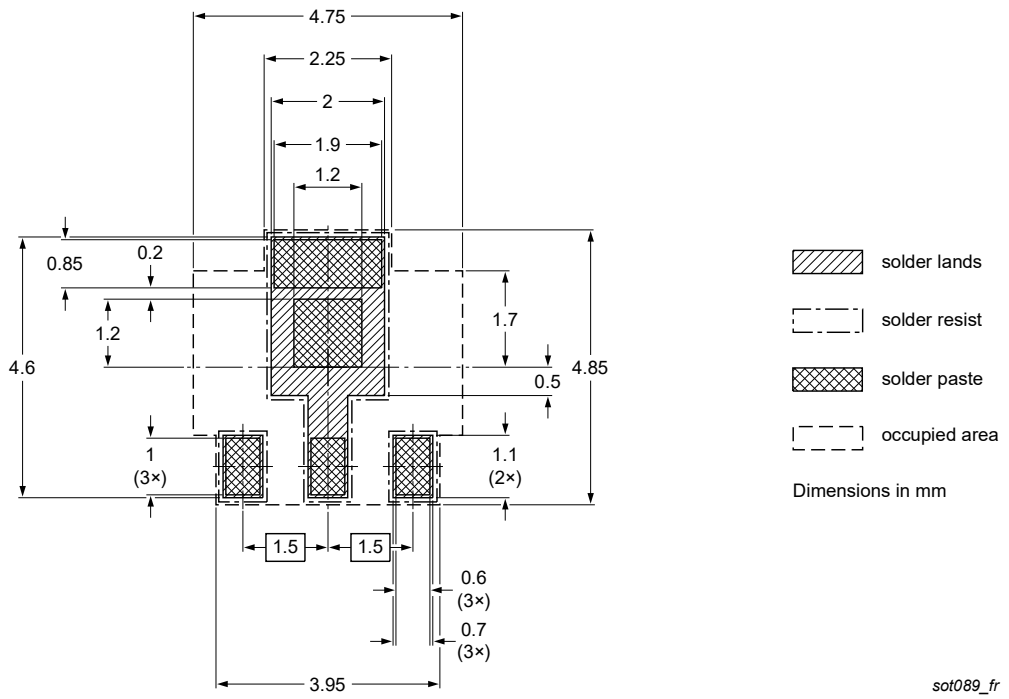


Fig. 4. Reflow soldering footprint for SOT89

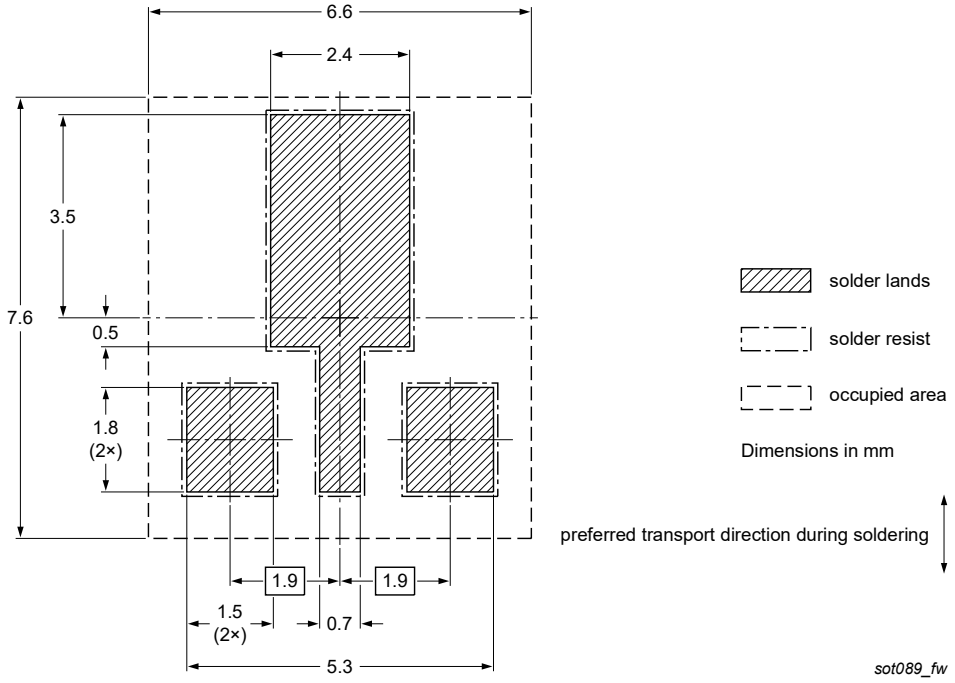


Fig. 5. Wave soldering footprint for SOT89

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BST60-Q v.1	20231027	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

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